

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	INTEGRATED CIRCUITS HAVING POST-SILICON ADJUSTMENT CONTROL							
Application Number :								
Date :								
First Named Applicant: Yoshiyuki Ando								
Confirmation Number:								
Attorney Docket Number: YA06								
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<table border="1"><tr><td>Submitted By:</td><td>Elec. Sign.</td><td>Sign. Capacity</td></tr><tr><td>Richard D. Fuerle Registered Number: 24640</td><td>Richard D. Fuerle</td><td>Attorney</td></tr></table>			Submitted By:	Elec. Sign.	Sign. Capacity	Richard D. Fuerle Registered Number: 24640	Richard D. Fuerle	Attorney
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Documents being submitted:	Files
us-fee-sheet	YA06-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-request	YA06-usrequ.xml us-request.dtd us-request.xsl
application-body	App-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd Table.tif Fig1.tif Fig2.tif Fig3.tif Fig4.tif Fig5.tif Fig6.tif Fig7.tif
us-declaration	Dec.tif
Comments	